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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Toshimitsu Taniguchi et al. Art Unit : 2811
Serial No. : 09/652,044 Examiner : Samuel A. Gebremariam
Filed : August 31, 2000
Title : SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING
THEREOF

Commissioner for Patents
Washington, D.C. 20231

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MAY 16 2002
PC-2800 MAIL ROOM

DECLARATION OF KAZUHIRO YOSHITAKE UNDER 37 CFR 1.48(c)

I, Kazuhiro Yoshitake, declare the following:

1. I am a co-inventor of the subject matter described and claimed in the present application.
2. The correction of inventorship is necessitated by amendment of the claims and such correction occurred without deceptive intent on my part or that of any of the actual inventors.
3. All statements made herein of my knowledge are true and that all statements made on information and belief are believed to be true; and further these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

Date: April 11, 2002

Kazuhiro Yoshitake
Kazuhiro Yoshitake

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL
I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

MAY 2, 2002
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